

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
1.0	EDITION ORIGINALE	2008-**-**	***

5 IMPEDANCE TABLE

LAYER	TRACE (mil)	SPACING (mil)	IMPEDANCE (Single end)	IMPEDANCE (Differential)	TOLERANCE
1,6	5.0	N/A	55 OHM	N/A	+/-10%
4	4.0				
1,6	4.8	10.2	N/A	90 OHM	+/-10%
			N/A	100 OHM	+/-10%

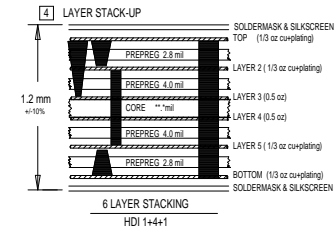
DRILL CHART: TOP to L2_GND					
ALL UNITS ARE IN MILS					
FIGURE	SIZE	TOLERANCE	PLATED	ROTATION	QTY
·	4.0	+3.0/-3.0	PLATED	-	76
TOTAL HOLES: 76					

DRILL CHART: TOP to L3_SIG					
ALL UNITS ARE IN MILS					
FIGURE	SIZE	TOLERANCE	PLATED	ROTATION	QTY
·	4.0	+3.0/-3.0	PLATED	-	63
TOTAL HOLES: 63					

DRILL CHART: L2_GND to L5_GND					
ALL UNITS ARE IN MILS					
FIGURE	SIZE	TOLERANCE	PLATED	ROTATION	QTY
·	8.0	+3.0/-3.0	PLATED	-	52
TOTAL HOLES: 52					

DRILL CHART: L5_GND to BOTTOM					
ALL UNITS ARE IN MILS					
FIGURE	SIZE	TOLERANCE	PLATED	ROTATION	QTY
·	4.0	+3.0/-3.0	PLATED	-	31
TOTAL HOLES: 31					

DRILL CHART: TOP to BOTTOM					
ALL UNITS ARE IN MILS					
FIGURE	SIZE	TOLERANCE	PLATED	ROTATION	QTY
·	8.0	+3.0/-3.0	PLATED	-	43
·	10.0	+3.0/-3.0	PLATED	-	317
·	12.0	+3.0/-3.0	PLATED	-	289
·	20.0	+3.0/-3.0	PLATED	-	2
·	33.0	+3.0/-3.0	PLATED	-	2
·	36.0	+3.0/-3.0	PLATED	-	50
·	40.0	+3.0/-3.0	PLATED	-	41
□	138.0	+3.0/-3.0	PLATED	-	4
·	28.0	+2.0/-2.0	NON-PLATED	-	3
·	39.0	+2.0/-2.0	NON-PLATED	-	4
·	79.0	+2.0/-2.0	NON-PLATED	-	2
·	33.0x30.0	+5.0/-5.0	PLATED	90.000	2
·	51.0x24.0	+5.0/-5.0	PLATED	0.000	2
·	59.0x33.0	+5.0/-5.0	PLATED	90.000	2
TOTAL HOLES: 763					

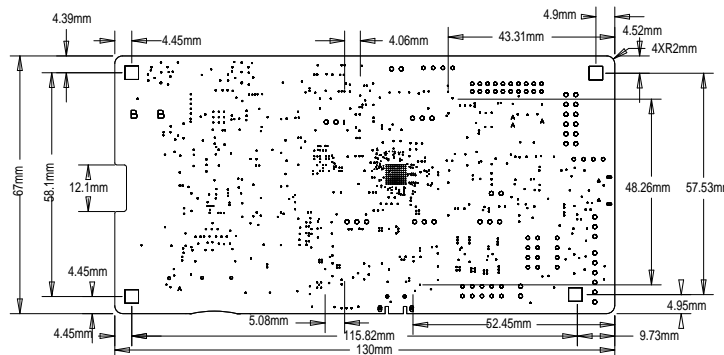


PCB SPECIFICATIONS:

- A. MATERIAL: FR-4, ☒ TG-170 ☐ TG-150 ☐ TG-140
- B. MATERIAL FAMILY: N/A
- C. SOLDERMASK COLOR: ☒ GREEN ☐ BLUE ☐ RED ☐ BLACK
- D. SILKSCREEN COLOR: ☒ WHITE ☐ YELLOW ☐ BLACK
- E. SURFACE FINISH: ☒ ENIG ☐ IMMERSION SILVER ☐ IMMERSION TIN
- ☐ HASL ☐ HASL(PB-FREE) ☐ GOLDEN FINGER
- F. IMPEDANCE CONTROL: ☐ NO ☒ YES (SEE IMPEDANCE TABLE FOR DETAIL INFORMATION)
- G. THROUGH VIA: PLUG THE VIAS WHICH ARE COVERED WITH SOLDERMASK ONE OR TWO SIDE.
- PLUG MATERIAL: ☒ SOLDERMASK ☐ NON-CONDUCTIVE EPOXY.
- H. STACK-UP: SEE LAYER STACK-UP SEQUENCE FOR OVERALL THICKNESS.

PCB REQUIREMENTS:

1. THIS BOARD WILL CONFORM TO:
IPC-A-600, CURRENT REV., CLASS II
IPC-6012, CURRENT REV., CLASS II
2. UNLESS OTHERWISE SPECIFIED ALL HOLE DIMENSIONS APPLY AFTER PLATING.
ALL HOLES SHALL BE LOCATED WITHIN .003" DIAMETER OF TRUE POSITION.
3. PLATED HOLE WALL THICKNESS SHALL NOT BE LESS THAN .001 INCH MINIMUM AVERAGE, WITH NO READING LESS THAN .0008 BY CORROSS SECTION.
4. MATERIAL FR4 RATING 94V-0 MINIMUM EPOXY GLASS LAMINATE.
5. BOARD SHALL BE LPI SOLDER MASKED OVER BARE COPPER BOTH SIDES PER IPC-SM-840 CLASS II.
6. SILKSCREEN SHALL BE PERMANENT NON-CONDUCTIVE INK AND WITH NO OVERLAP ON ANY COMPONENT PAD OR THROUGH HOLE.
7. MFGR. TO LEGIBLY ETCH OR STAMP/SCREEN WITH PERMANENT NON-CONDUCTIVE INK
A. U.L. CODE D. MFGR. LOGO
B. DATE CODE E. SUCCESSFUL ELECTRICAL BOARD TEST.
C. FLAMMABILITY RATING
8. REMOVE THE FLASHS WHICH SMALLER THAN HOLE SIZE.
9. REMOVE ALL SHAPE EDGES AND BURRS .005 MAXIMUM.
10. PLEASE USE THE SUPPLIED IPC 356 NETLIST TO VERIFY BOARD BEFORE FABRICATING BOARD.



 DESIGNED BY SOFER www.sofer.com.cn Tel: +86 21-6482 6908 Mail: design@sofer.com.cn	LAYER: FAB DAWING & DRILL LEGEND	COMPANY NAME: ST
	DESIGNER: Shelley	PROJECT NAME: MB1261
	DATE: 2016.09.30	PROJECT NUMBER: D031-023A2
		REV B

UNLESS OTHERWISE SPECIFIED		SIGNATURES		DATE		LOGO		COMPANY NAME	
DIMENSIONS ARE IN INCHES TOLERANCES ON: 2 PL DECIMALS + 3 PL DECIMALS + ANGLES + FRACTIONS +		DRAWN		YY-MM-DD		TITLE		PROJECT NAME *	
		CHECKED		YY-MM-DD					
		ENGRG		YY-MM-DD					
		ISSUED							
						SIZE B		DWG NO *****	
						SCALE 1:1		SHEET 1 OF 1	